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**DUANE MORRIS LLP  
IP DEPARTMENT (TSMC)  
30 SOUTH 17TH STREET  
PHILADELPHIA PA 19103-4196**

Patent No. : 7,079,412 B2  
Inventor(s) : Chung-Hui Chen, et al.  
Issued : July 18, 2006  
For **PROGRAMMABLE MOS DEVICE  
FORMED BY STRESSING  
POLYCRYSTALLINE SILICON**  
Doc. No. N1280-00100

To Whom It May Concern:

The Certificate of Correction issued on July 24, 2007, issued in error, in that error(s) was made in identifying the patent number and/or keying text/corrections, i.e.:

The issue date in the heading of cofc has been incorrectly entered in the heading of the issued cofc. A superseding cofc will be issued to correct the patent issue date to read --July 18, 2006--.

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Therefore, a certificate of correction will be issued to correct (supersede) the Certificate of Correction containing error(s), made during preparation of the Certificate of Correction, as noted above.

No further response is required, from applicants (attorney). However, errors discovered by attorney, other than as noted and described above, should be noted on *a copy* of the Certificate of Correction that was issued in error, accompanied by a signed transmittal letter and submitted directed to this Branch.

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cbn